

EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

This communication is an Examiner's reasons for allowance in response to application filed on 02/10/2006, assigned serial 10/567,870 and titled "Map Information Processing Device"

The following is the Examiner's statement of reasons for the indication of allowable subject matter:

The applicant's remarks filed on 06/5/2008 have been fully considered. After carefully reconsidering the application and the applied prior art, the examiner has realized the application is patentably distinct from the prior art. The prior art are not deemed strong to make the application unpatentable.

The examiner has reviewed the art rejection and the prior art applied in the rejection. The examiner has recognized the cited prior art do not fairly disclose or suggest an intersecting judging means for identifying intersection points where the new road detected by said new road detecting means intersects either roads included in the map information acquired by said map information acquiring means or roads previously detected by said new road detecting means, other than start and end points of the new road; a road update information generating means for generating new road information indicating the new road detected by said new road detecting means, and for generating road update information including information for updating said new road information stored in the new road information storage means and the road information included in the map information stored in said map information storage means; and a road information updating means for updating the road information included in the map

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information stored in said map information storage means, and road information included in the new road information stored in said new road information storage means based on the road update information generated by said road update information generating means, and for writing the new road information into said new road information storage means.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan C To whose telephone number is (571) 272-6985. The examiner can normally be reached on from 8:00AM to 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Keith can be reached on 571-272-6878.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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/Tuan C To/

Primary Examiner of Art Unit 3663/3600

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